

Analog Design Engineer

Qorvo is All Around You! We manufacture innovative RF solutions at the center of connectivity to enable high-performance applications for advanced wireless devices, wired and wireless networks and defense radar and communications for 5G networks, cloud computing, the Internet of Things, and other emerging applications. We are a global company thriving on a culture of innovation, diversity, and inclusion. Our people are always at the core of Qorvo's innovation. Qorvo is a place to stretch your imagination and push boundaries to achieve success.

It is an exciting time to be a part of Qorvo's Infrastructure and Defense Products. Qorvo's IDP manufactures and markets high performance RF and mixed signal integrated circuits for the communications markets that connect, protect and power the world. Our customers trust Qorvo to meet and exceed requirements and deliver advanced RF technologies to enable their most complex technical challenges and applications. Our products address connectivity both wired and wireless networks, the smart home and the IoT, broadband, aerospace, defense, and automotive. Our customers rely on us to solve the industry's most complex engineering problems. And that takes brain power and willingness to create and innovate. Join us and help us continue to create cutting edge technologies for a more connected world all around you.

Qorvo Vietnam was established in 2004 and is an emerging leader in multi-billion-dollar power management and intelligent motor drive IC markets. The portfolio of analog and mixed signal System on Chips provides scalable core platforms used in charging and powering embedded digital control systems for industrial, commercial and consumer applications. Their expertise in Power Application Controllers (PAC) and Programmable Analog ICs significantly reduce solution size and cost, improve system reliability and shorten system development cycle-time. The team, which consists of a young and vibrant workforce innovates and enables technology breakthrough in the space of analog and mixed signal integrated circuit design.

Site: Vietnam – Hanoi

Business Title: Design Engineer

Shift: Normal Day

RESPONSIBILITIES:

Qorvo's Power Management team designs a broad range of products which include Power Management Units (PMUs) for various applications such as Smartphones, notebooks, Solid State Drive, Digital Camera, IoT and wearable devices to battery management (linear and switching charger for different battery types) which is a System on Chip that focus on high efficiency motor controller for wide range of application such as home appliances, whitegoods, power tools, garden tools, drones.

Analog Design Engineer participates in designing cutting edge, high performance and reliable circuits such as high precision (bandgap, oscillators, crystal driver, voltage and current sources) , very low/high power, high efficiency converters (both linear and switching regulators), data converters, battery management, ESD/Latch up immune design, design for testability, mixed mode signal simulation and works closely with the layout team to meet the design target. Analog design engineer also collaborates with the Test development, QRA team, and operation to ensure a seamless product release to market.

QUALIFICATIONS:

- Bachelor Degree in Electrical Engineering or equivalent
- Strong foundation in electronic circuit design and analysis
- Good problem solving and de-bugging skills
- Strong written and verbal communication skills
- Proficient in both Vietnamese and English (Read and Write)
- Self-motivated and able to work independently during high pressure
- Minimum four or more years of working years with experience one or more of the following areas is required:
 - Power ICs
 - High voltage analogue design
 - AC-DC controllers, fly-back primary side regulator
 - DC-DC controllers, LDO, buck, boost, charge pump
 - Battery management (linear, switching charger)
 - A/D and D/A converter
 - Experience in SoC design or embedded system
 - Experience in RTL and behavioral coding
 - Experience designing in I2C and SPI interfaces or other peripheral interfaces